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Description

The FS8108 is a serial data input, phase-locked loop IC with programmable input and reference frequency dividers. When combined with a VCO, the FS8108 becomes the core of a very low power frequency synthesizer well-suited for mobile communication applications such as paging systems. The FS8108 includes an 18-bit programmable input frequency divider and also implements a separate pin for stand-by control.

Features

- ◆ High maximum input operating frequency — 185 MHz at $V_{DD1} = 1.0$ V
- ◆ Up to 22 MHz internal crystal oscillator reference frequency at $V_{DD1} = 1.0$ V
- ◆ Extremely low current consumption ($I_{DD, total}$ typically 0.4 mA at $f_{FIN} = 90$ MHz)
- ◆ 18-bit programmable input frequency divider (including a $\div 64/65$ prescaler) with divide ratio range from 4032 to 262143
- ◆ 13-bit programmable reference frequency divider (including a $\div 8$ prescaler) with divide ratio range from 40 to 65528
- ◆ Optional lock detector output
- ◆ Charge pump output for passive low-pass filter
- ◆ Quick-lock signal output for faster locking
- ◆ Separate pin for stand-by control
- ◆ TSSOP 16L package (0.65mm pitch)

Applications

- ◆ Pager
- ◆ Wireless communication system