DESCRIPTION

The PT1621 is a RAM mapping, 128 dots (32x4) and multi-function LCD driver. The host can program PT1621 easily by three or four lines interface. Many S/W configuration features make PT1621 suitable for multiple LCD applications. PT1621 also includes S/W power down command to reduce power consumption.

FEATURES

- Operating voltage: 2.2V~5.5V
- LCD operating voltage can be adjusted by VLCD pin
- Built-in 256KHz RC oscillator
- External 32.768KHz crystal or 256KHz frequency source input
- Two selectable tone (buzzer) frequencies (2KHz/4KHz)
- Built-in time base generator and WDT
- Time base or WDT overflow output
- 8 kinds clock sources of time base/WDT
- Internal LCD driving frequency source
- Selection of 1/2 or 1/3 bias, and selection of 1/2 or 1/3 or 1/4 duty LCD applications
- 32 x 4 LCD driver
- Built-in POR circuit
- Built-in 32 x 4 bit display RAM
- 3 or 4-wire serial interface
- Power down command reduces power consumption
- Software configuration feature
- Data mode and command mode instructions
- R/W address auto increment
- Three data accessing modes
- Package type:
  - PT1621-LQ: 44pin LQFP
  - PT1621-X: 48pin SSOP
  - PT1621-G: Gold bumped chip

BLOCK DIAGRAM