

DESCRIPTION

The PT9120 is a single chip Global Positioning System (GPS) receiver front-end IC requiring few external components and offering extremely low power consumption. The PT9120 employs a super-heterodyne receiver topology which down-converts the 1575.42MHz L1-band GPS signal to a 1st IF. The 1st IF is then filtered by an off-chip L-C filter and subsequently sub-sampled by the 2-bit A/D converter to provide both sign and magnitude quantized CMOS level outputs to base band inputs.

FEATURES

- GPS L1-band (C/A code) receiver
- Integrated LNA and antenna detector
- Fully-monolithic VCO
- Support for several reference frequencies
- 2-bit ADC output (sign and magnitude)
- Extremely low current consumption (7mA at AVDD=DVDD=2.5V)
- Multiple power-down modes
- Available in 28 pins or 24 pins, QFN package

APPLICATION

- GPS systems

BLOCK DIAGRAM

